

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Amir Alon
Serial No: 10/091,934
Filed: March 6, 2002
For: An Interconnect-Aware Methodology
For Integrated Circuit Design

Art Unit: 2825
Examiner: Levin, Naum B.
Conf. No.: 7058

AFTER FINAL AMENDMENT

Mail Stop AF
Commissioner for Patents
PO BOX 1450
Alexandria, VA 22313-1450

Dear Sir:

I hereby certify that this correspondence is
being deposited by way of **ELECTRONIC
FILING** (or **FACSIMILE** to 571 273 8300)
with the United Patent Office addressed to:

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

on:

February 29, 2008

Date of Deposit

F. Jason Far-hadian, Esq.

Name

/F. Jason Far-hadian/ 02 - 29 -08

Signature

Date

Introductory Comments:

This is in response to the Office Action mailed on November 30, 2007, in the above-captioned application. This application is amended by the virtue of this response, pursuant to 37 CFR 1.116. Please amend the application as provided below. The consideration of the amended claims and the remarks provided below is respectfully requested as they place the application in condition for allowance or better form for appeal.